

AMENDMENTS TO THE SPECIFICATION

Please amend the specification as indicated below. The language being added is underlined (“ ”) and the language being deleted contains a strikethrough (“”).

Please substitute the following annotated paragraph for paragraph [0039]:

In at least one embodiment, the N low-order bits of registers rA and rB (each having a total of S bits) represent values to be compared whereas the remaining high-order bits (i.e., S-1 to N) are irrelevant for comparison purposes. Accordingly, at step 102 the processor performs an unsigned comparison using, for example, a comparator on the N low-order bits of rA and rB (designated as rA[N-1:0] and rB[N-1:0], respectively). If the unsigned value stored in rA[N-1:0] is determined to be less than or equal to the unsigned value stored in rB[N-1:0] (step 104), then the processor is manipulated to ~~either~~ store the entire value (i.e., rA[S-1:0]) to the destination register rD at step 106, thereby setting the value stored in rD equal to the value stored in rA.